&TDK Multilayer Ceramic Chip Capacitors

C3225JB1C106M200AA



TDK item description C3225JB1C106MT****

		N/4
Applications	Commercial Grade	
Feature	General General (Up to 50V)	
Series	C3225 [EIA 1210]	4
Status	Production (Not Recommended for New Design)	Dimensione in mm

	Size
Length(L)	3.20mm ±0.40mm
Width(W)	2.50mm ±0.30mm
Thickness(T)	2.00mm ±0.20mm
Terminal Width(B)	0.20mm Min.
Terminal Spacing(G)	
Recommended Land Pattern (PA)	2.00mm to 2.40mm
Recommended Land Pattern (PB)	1.00mm to 1.20mm
Recommended Land Pattern (PC)	1.90mm to 2.50mm

Electrical Characteristics		
Capacitance	10µF ±20%	
Rated Voltage	16VDC	
Temperature Characteristic	JB(±10%)	
Dissipation Factor (Max.)	5%	
Insulation Resistance (Min.)	10ΜΩ	

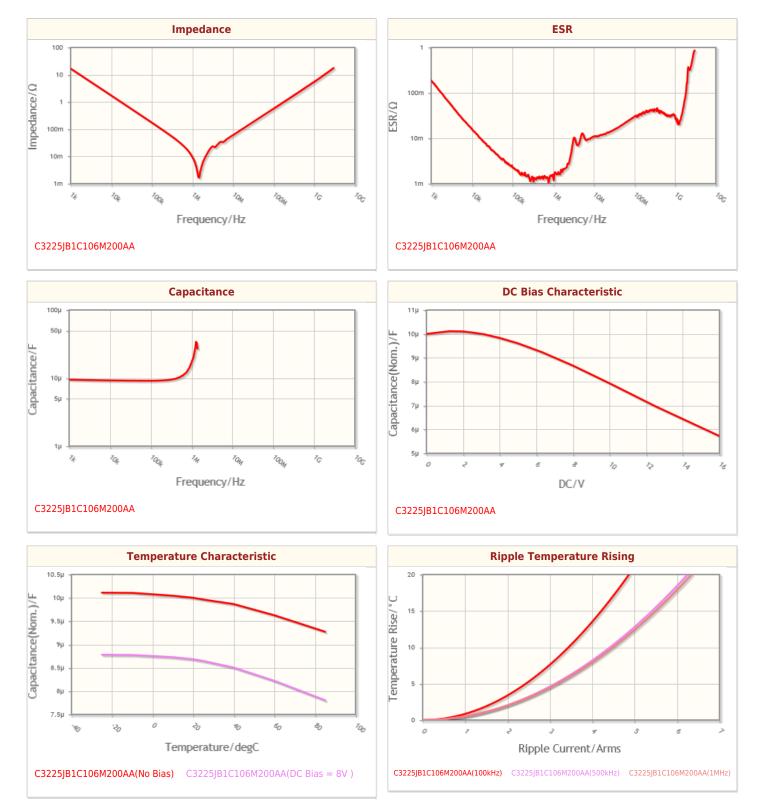
Other	
Soldering Method	Reflow
AEC-Q200	No
Packing	Blister (Plastic)Taping [180mm Reel]
Package Quantity	1000pcs

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! All specifications are subject to change without notice.

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Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

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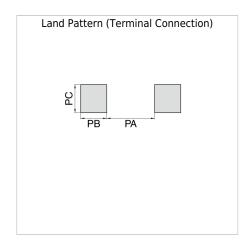
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Associated Images



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